HP 13255

SIMPLIFIED KEYBOARD MODULE

Manual Part No. 13255-91069

PRINTED

AUG-01-76

DATA TERMINAL TECHNICAL INFORMATION





1.0 INTRODUCTION.

The Simplified Keyboard Module substitutes the Simplified Keyboard Assembly (02640-60068) and the Simplified Keyboard PCA (02640-60069) for the General Purpose Keyboard Assembly (02640-60030) and the Keyboard PCA (02640-60018). Parts lists and detailed description for the Keyboard Interface PCA (02640-60019) and the cable assemblies are contained in module section 13255-91018.

2.0 OPERATING PARAMETERS.

A summary of operating parameters for the Simplified Keyboard Module is contained in tables 1.0 through 5.3.

Size $(L \times W \times D)$ Part 1 Weight I Number Nomenclature 1 +/-0.100 Inches I (Pounds) I 02640-60019 | Keyboard Interface PCA $12.9 \times 4.0 \times 0.5$ 0.38 02640-60041 | Speaker Cable Assembly N/A N/A 02640-60068 ! Simplified Keyboard Assembly | N/A N/A 02640-60069 | Simplified Keyboard PCA $16.8 \times 7.1 \times 2.1$ 2.75 02640-60081 | Keyboard Cable Assembly N/A N/A Number of Backplane Slots Required: 1

Table 1.0 Physical Parameters

HP 13255

SIMPLIFIED KEYBOARD MODULE

Manual Part No. 13255-91069

PRINTED

AUG-01-76

NOTICE

The information contained in this document is subject to change without notice.

HEWLETT-PACKARD MAKES NO WARRANTY OF ANY KIND WITH REGARD TO THIS MATERIAL, INCLUDING, BUT NOT LIMITED TO THE IMPLIED WARRANTIES OF MERCHANIABILITY AND FITNESS FOR A PARTICULAR PURPOSE. Hewlett-Packard shall not be liable for errors contained herein or for incidental or consequential damages in connection with the furnishing, performance, or use of this material.

This document contains proprietary information which is protected by copyright. All rights are reserved. No part of this document may be photocopied or reproduced without the prior written consent of Hewlett-Packard Company.

Copyright c 1976 by HEWLETT-PACKARD COMPANY

NOTE: This document is part of the 264XX DATA TERMINAL product series Technical Information Package (HP 13255).

1.0 INTRODUCTION.

The Simplified Keyboard Module substitutes the Simplified Keyboard Assembly (02640-60068) and the Simplified Keyboard PCA (02640-60069) for the General Purpose Keyboard Assembly (02640-60030) and the Keyboard PCA (02640-60018). Parts lists and detailed description for the Keyboard Interface PCA (02640-60019) and the cable assemblies are contained in module section 13255-91018.

2.0 OPERATING PARAMETERS.

A summary of operating parameters for the Simplified Keyboard Module is contained in tables 1.0 through 5.3.

Size (L x W x D) Part Weight | +/-0.100 Inches I (Pounds) I Number Nomenclature 1 02640-60019 | Keyboard Interface PCA $12.9 \times 4.0 \times 0.5$ 0.38 02640-60041 | Speaker Cable Assembly N/A N/A 02640-60068 | Simplified Keyboard Assembly | N/A N/A 02640-60069 | Simplified Keyboard PCA $16.8 \times 7.1 \times 2.1$ 2.75 02640-60081 | Keyboard Cable Assembly N/A N/A Number of Backplane Slots Required: 1

Table 1.0 Physical Parameters

Table 2.0 Reliability and Environmental Information

==				========	=======		======
1							
1	Environmental:	(X) HP	Class B	() (Other:		
1	Restrictions: 1	Type tested a	it product	level			
! ! =	=======================================		:=======	=======================================	========		::::::
1	Fai	llure Rate:	1.542	(percent	per 1000	hours)	

Table 3.0 Power Supply and Clock Requirements - Measured (At +/-5% Unless Otherwise Specified)

+5 Volt Supply +12 Volt Supply	-12 Volt Supply -42 Volt Supply
@ 400 mA @ 100 mA	e 80 mA l
i i	NOT APPLICABLE
115 volts ac	220 volts ac
а д	e A
NOT APPLICABLE	NOT APPLICABLE
Clock Frequency:	MHZ
ITA TON	PLICABLE

Table 4.0 Connector Information

=======================================	* *** *** *** *** *** *** *** *** ***	
I Connector	Signal	Signal
I and Pin No. 1	Name	Description
	GND	
1	!	} Ground
- 2	GND	
- 3	BADDR3	Positive True, Column Address Bit 3
- 4	BBUS4	Negative True, Buffered Data Bus Bit 4
- 5	RBUS5	Negative True, Buffered Data Bus Bit 5
- 6	BBUS7	Negative True, Buffered Data Bus Bit 7
- 7	RBUS6	Negative True, Buffered Data Bus Bit 6
- 8	READ . COL15	Negative True, Enables Reading the Column 0-13. Not asserted for Columns 14 & 15
- 9		Not Used
-10	LED EN	Strobes Data into LED Latches
-11	REEP	Trigaers Beeper Circuit
-12	+127	+12 Volt Power Supply
-13	+5 V	+5 Volt Power Supply
-14	-12V	-12 Volt Power Supply
-15	CHASSIS GND	Grounds the Switchplate
	 -===================================	

Table 4.1 Connector Information

Connector and Pin No.	Signal Name	Signal Description
======================================		====================================
J4, Pin 1	PON	Pesets the Terminal
- 2	+5 V	+5 Volt Power Supply
- 3	COL OUT EN	Strobes Data into Column's Previous State in Input Register
- 4 I	BBUS3	Negative True, Buffered Data Bus Bit 3
- 5 I	BRUS0	
- 6 I	BBUS2	
- 7 i	BBUS1	 Negative True, Buffered Data Bus Bit 1
- 8	BADDR2	Positive True, Column Address Bit 2
- 9	BADDR1	Positive True, Column Address Bit 1
-1 0	BADDRO	Positive True, Column Address Bit 0
i		I

Table 5.0 Module Bus Pin Assignments

Function		rable 3.		5	
Performed: Output a column's previous state into Simplified Keyboard PCA's input register				=======	
Simplified Keyboard PCA's input register	1			1	
Poli Bit: Not Applicable X ADDR 15 Module Address: (ADDR 11,10,9,4) = (0011) X ADDR 12 Module Address: (ADDR 11,10,9,4) = (0011) X ADDR 12 O ADDR 11 O ADDR 11 O ADDR 10 1 ADDR 9 Function Specifier: ADDP 5 = 1 X ADDR 9 Function Specifier: ADDP 5 = 1 X ADDR 9 O ADDR 7 X ADDR 6 1 ADDR 5 1 ADDR 5 1 ADDR 5 1 ADDR 3 X ADDR 3 X ADDR 3 X ADDR 3 X ADDR 2 Data Bus Bit Interpretation: Fach data bit is X ADDR 3 X ADDR 3 X ADDR 0 1 ADDR 1 2 ADDR 1 3 ADDR 0 3 ADDR 1 4 ADDR 0 5 BUS 0 6 BUS 0 6 BUS 0 6 BUS 0 6 BUS 0 7 ADDR 1 8 BUS 0 9 BU	!			Value	Signal
Poll Bit: Not Applicable	•	Simplified Key	board PCA's input register	======	
Module Address: (ADDR 11,10,9,4) = (0011)	!				
Module Address: (ADDR 11,10,9,4) = (0011)	!	Poll Bit: Nor Applicable			
C	!				
Function Specifier: ADDR 5 = 1	!	Module Address: (ADDR 11,	10,9,4) = (0011)		
Function Specifier: ADDR 5 = 1	!				
Function Specifier: ADDR 5 = 1 X	ı				
	1				
X	ı	Function Specifier: ADDR	5 = 1		
1	ł				
1	f			i X	•
X	1			i 1	
Data Bus Bit Interpretation: Fach data bit is associated with a switch in a column. If X ADDR 1 ADDR 0 the bit is set to 1, it indicates that the switch was previously depressed. The column to which the value is applied is specified B6 BUS 7 BUS 7 BUS 8 BUS 8 BUS 8 BUS 9 BUS 9	ł			l 1	ADDR 4 I
Data Bus Bit Interpretation: Each data bit is associated with a switch in a column. If X ADDR 0 the bit is set to 1, it indicates that the switch was previously depressed. The column B7 BUS 7 to which the value is applied is specified B6 BUS 6 bv a subsequent switch read as indicated B5 BUS 5 in table 5.1. B4 BUS 4 BUS 4 B3 BUS 3 B2 BUS 5 B1 BUS 1 B2 BUS 0 B1 BUS 1 B0 BUS 0 B1 BUS 0 B1 BUS 1 B2 B2 B3 B3 B3 B3 B3 B3	1			l X	ADDR 3
associated with a switch in a column. If	t		;	1 X	ADDR 2
the bit is set to 1, it indicates that the switch was previously depressed. The column to which the value is applied is specified B6 BUS 6 B0S 6 B0S 6 B0S 6 B0S 5 B0S 5	1	Data Bus Bit Interpretatio	n: Fach data bit is	j X	ADDR 1
the bit is set to 1, it indicates that the switch was previously depressed. The column to which the value is applied is specified B6 BUS 6 B0 a subsequent switch read as indicated B5 BUS 5 B0 a subsequent switch read as indicated B4 BUS 4 B0 a subsequent switch read as indicated B5 BUS 5 B1 a subsequent switch read as indicated B5 BUS 5 B1 a subsequent switch read as indicated B6 BUS 6 B1 a subsequent switch read as indicated B6 BUS 6 B1 a subsequent switch read as indicated B6 BUS 6 B1 a subsequent switch read as indicated B6 BUS 6 B1 a subsequent switch read as indicated B6 BUS 6 B1 a subsequent switch read as indicated B6 BUS 6 B1 a subsequent switch read as indicated B6 BUS 6 B1 a subsequent switch read as indicated B6 BUS 6 B1 a subsequent switch read as indicated B2 B1 a subsequent switch read as indicated B2 B1 a subsequent switch read as indicated B2 B2 B2 B2 B2 B2 B2 B	1	associated with a swi	tch in a column. If	ı x	ADDR 0 I
switch was previously depressed. The column to which the value is applied is specified	1			======	
to which the value is applied is specified B6 BUS 6 by a subsequent switch read as indicated B5 BUS 5 in table 5.1.	1			B7	RUS 7
bv a subsequent switch read as indicated	1				
in table 5.1. B4	1				
B3	1	in table 5.1.			
B2	1		1		
B1 BUS 1 B0 BUS 0	1				•
B0	i				
	i				
I=Logical 1=Bus Low I0=Logical 0=Bus High X=Don't Care	i				
10=Logical 0=Bus High X=Don't Care	i			•	•
! IX=Don't Care	i				
	i				
	i				_
	i		•		
	i				1
	1				!
	•				!
	1				1
	•				
	1				!
	!				!
	!				Į.
	1				1
	!				!
	!				1
	1				İ
	1				Į.
	ı				#
	1				1
	1				1
	1				1
	===:				

Table 5.1 Module Bus Pin Assignments

===:	=======	====:	=====	=====	=====	======	=====	=====	=====	=====	=====	======	======
ŧ	Function	วก								1	1	Bu	ıs i
1	perform	ned:	Read	swite	ches in	colum	"ת" מו	as		1 Vá	alue I	Sigr	al i
1							A1, ar			==:	====1	======	•
i						,				i	X i	ADDE	≀ 15 i
i	Poll Bi	it:	Not 1	Applic	ahle					i	Χİ	ADDE	- •
i				POTI	LODIC					i	x i	ADDE	
	Module	Addr	0661	CADD	5 11 17	1 6 41	- (001	113		,	χ̈́	ADDE	
	Module	MOUL	C35.	(ADD)	. 1191	1,7,41	- (00))		,	ô i		
1										1	-	ADDF	- •
!											0 1	ADDE	-
!	P/1 = - 4 4 4					4 2 2			_	!	1 !	ADDE	•
!	Function	on Spe	ectite				, are t)	!	X	ADDF	_ •
!							n keybo			!	0 1	ADDE	•
•							be rea			!	X I	ADDE	•
1							r spec			Į.	X I	ADDF	•
1				ì	e les:	s than	14 (de	ecimal)	1	1 1	ADDF	•
1										1 ,	43 I	ADDE	3 1
1											A2 1	ADDE	₹ 2
•	Data Bu	is Bit	t Inte	erpret	tation	: Fact	n data	bit is	5	1 1	A1 1	ADDE	₹ 1
ŧ	ass	sociat	ted wi	ith a	switch	n in a	column	. If		1 /	40 I	ADDE	8 0 1
1	the	e swi	tch is	s depi	ressed.	, the d	data bi	it is	1.	==:	====!	======	=====
1	(Re	efer	to fid	gure 1	l for a	a cross	s-refer	ence o	o f	1 1	3 7 1	BUS	7
1	kes	y numi	bers 1	to the	phys:	ical sv	witche:	on the	ne	1 1	36	BUS	6
1		boar								1 (35 I	BUS	5
1											1 B4 1 BUS 4		•
i											1 B3 1 BUS 3		
i											1 B2 1 BUS 2		•
i											I B1 I BUS 1		
i											30 1	BUS	•
i										1-=		======	
ì										11-1		1 1=Bus	
												1 0=Bus	
													o nigni
1	======				_)on't	:====== Cate	!
:	_				-			DATE I	11C DT9				!
•	1 001	Lumn	Addre	= S S	! 			DAJA 1	BUS BIT	l 			_
!	1====							:					- 1
!			A1								B1	1 BO	
!	-		-		•		-	-		•	•	: =====	
!	1 0	1 0	0									1 000	
!	1 0	1 0	1 0	1 1			015					1 010 1	
!	10										021	1 020	
1	10	1 0	1 1	1 1		036	Ī		033			1 030 1	1
1	10	1 1		0	. •	046	-	044				1 040 1	i
1	10	1	1 0	1 1	057	-	-	054		052		1 050 1	1
1	10	1 1	1 1	10	-	- 1	-	064		062		i - 1	1
ı	10	1 1	1 1	1 1	-	- 1	075			072	071	1 070 1	1
1	1 1	0	0	0 1	- !	ı - !	105			102	101	1 100 1	1
1	1 1	1 0	1 0	1 1	-	1116		114				1 110	1
1	1 1	0	1 1	1 0	,	126	125	124	123	122	121	1 120	1
1	1 1	1 0	1 1	1 1		136	-	134	1 1 3 3	132	131	1 130 1	1
1	1 1	1 1	1 0	1 0 (147	- 1	- 1	144		142		1 140	i
1	1 1	1 1	0	1 1	157	ı - i	ı - 1	1.54	- 1	152		1 150	-
===:	======	=====	====:	=====	=====		=====	=====	=====	:====:	=====	=====	.=====

Table 5.2 Module Bus Pin Assignments

Function	===:	=======================================			
Performed: Keyboard PCA. (Pefer to figure 1 for physical location of data comm switches and their positions.)	1	Function	Read data comm switches on Simplified	1	l Bue i
Dhvsical location of data comm switches and their positions.)	1	Performed:		I Value	
and their positions.) Poll Bit: Not Applicable Poll Bit: Not Applicable Module Address: (ADDR 11,10,9,4) = (0011) Module Address: (ADDR 11,10,9,4) = (0011) Punction Specifier: ADDR 0,1,2,3 = (1111) Data Bus Bit Interpretation: Data Bus Bit Interpretation: Switch 1 ======== Position 0 1 1 1 1 1 1 1 1 1	1			=======	!=====================================
Poll Bit: Not Applicable	1			X	ADDR 15
Poll Bit: Not Applicable	1		,		
Module Address: (ADDR 11,10,9,4) = (0011)	1	Poll Bit:	Not Applicable		
Module Address: (ADDR 11,10,9,4) = (0011) Function Specifier: ADDP 0,1,2,3 = (1111) Function Specifier: ADDP 0,1,2,3 = (1111) Data Bus Bit Interpretation: Data Bus Bit Interpretation: Data Bus Bit Interpretation: 1	1			I X	
Function Specifier: ADDP 0,1,2,3 = (1111) Function Specifier: ADDP	1	Module Addre	ess: $(ADDR 11,10,9,4) = (0011)$	1 0	
Function Specifier: ADDR 0,1,2,3 = (1111)	1			0	ADDR 10
O	1				ADDR 9
Data Bus Bit Interpretation:	1	Function Spe	ecifier: ADDR $0,1,2,3 = (1111)$	•	•
Data Bus Bit Interpretation:	!			•	
Data Bus Bit Interpretation: 1	!				
Switch 1	!				
Switch 1	!	Data Bus Bit	t Interpretation:	-	
Position 0 1	!				
Position 0 1	1		Switch 1		
B7 1 1 0	!	01-1		*	
B7	!	Position	1011	1 1	
B6	!	() "7		1======	•
B5	1	b /	• - • • •		
B6	1				•
Switch 2 ===================================	i	96 3	Not sectioned slugge 0		
Switch 2 ====================================	i	80 ,	tot assigned, always v		
Position 0 1 2	i		Switch 2		-
Position 0 1 2	i			· -	· · · · · · · · · · · · · · · · · · ·
B5	i	Position	1 0 1 1 1 2 1		
B4	i			1======	
B4	1	<u> </u> §5	1010111	1=Logica	al 1=Pus Low i
X=Don't Care	1		1011101		
Switch 3	1				
Position 0 1 2 3 4 5 6 7	1			=======	
B3	1		Switch 3		1
B3	1	. v			1
B2	!	Position	10111213141516171		1
B2	!				1
B1	!	_			!
	!				1
BO Not assigned, always O	!	81			!
BO Not assigned, always O	!				1
BO Not assigned, always O	!				!
BO Not assigned, always O					;
	i	۵n ۷	int assigned always 0		
	i	יג טם	ioc destaucht aimaks n		Ţ E
, 	i				· · · · · · · · · · · · · · · · · · ·
, 	i				; 1
	i				, 1
	====			=======	

Table 5.3 Module Bus Pin Assignments

Function Write LED latch and trigger alarm	1	l Bus
Performed: generator (Beep)	l Value	
	======	•
- • • • • • • • • • • • • • • • • • • •	l X	ADDR 15
Poll Bit: Not Applicable	į X	ADDR 14
	l X	ADDR 13
Module Address: $(ADDR 11, 10, 9, 4) = (0011)$	i X	ADDR 12
	1 0	ADDR 11
	1 0 1 1	I ADDR 10 I ADDR 9
Function Specifier: ADDR 5 = 0	i X	-
Function Specifier: Abbk 5 - 0	1 0	I ADDR 8 I ADDR 7
	ix	I ADDR 6
	i ô	ADDR 5
	1 1	I ADDR 4
	i X	ADDR 3
	Ϊ́x	I ADDR 2
Data Bus Bit Interpretation:	i x	I ADDR 1
baca bus bit interpretation.	i x	I ADDR 0
B7	1======	1========
when Set, Beeper is triggered	i B7	BUS 7
mich becy becker in cringerou		I BUS 6
	1 B5	I BUS 5
B6	1 B4	BUS 4
when Set, LED #7 is turned on	1 83	I BUS 3
		I BUS 2
	I B1	I BUS 1
B5	1 B0	I BUS 0
When Set, LED #6 is turned on	======	
	1=Logic	al 1=Bus Lov
		al O=Bus Hi
B4	IX=Don't	
when Set, LED #5 is turned on	2222222	==========
B3		
When Set, LED #4 is turned on		
B2		
when Set, LED #3 is turned on		
B1		
Not Used		
7 3		
BO Set (F) 41 is turned on		
when Set, LED #1 is turned on		

FUNCTIONAL DESCRIPTION. Pefer to the switch location diagram (figure 1), block diagram (figure 2), schematic diagram (figure 3), timing diagram (figure 4), component location diagram (figure 5), and parts lists (02640-60068 and 02640-60069) located in the appendix.

The Simplified Keyboard PCA consists of a column decoder, an 8 by 14 key matrix, ramp generator, differential comparator circuits, an output register (data taken by processor), an input register (receives previous state of the key switches from processor), data comm logic, and a LED register.

- 3.1 COLUMN DECODER. The column decoder selects one column in the key matrix when binary address ADDRO through ADDR3 is applied.
- 3.2 KEY MATRIX.
- 3.2.1 The key switches are arranged in a matrix of 8 rows and 14 columns. The matrix is scanned column by column, so that eight switches at a time are read.
- 3.2.2 The key switch used is a LICON type consisting of a ferrite core, a drive wire, a sense wire, and two magnets. When the switch is in the undepressed state, the two magnets are in close proximity to the core, thus saturating the core and inhibiting the coupling of a signal from the drive wire to the sense wire. When the switch is depressed, two magnets are moved away from the core and a signal is coupled from the drive wire to the sense wire. All switches in one column are connected serially by drive lines and switches in one row are connected serially by sense lines. One side of the drive lines is connected to the column decoder (U5 and U7), and the other side to the ramp generator (to collector of Q2). Eight sense lines are connected to differential comparators on one side and are grounded on the other end. After the column decoder selects a column and the ramp generator is enabled, then drive current (80 milliamperes) flows through the selected column. Depressed keys in the selected column couple the drive signal to the sense line which is then applied to the differential comparator.

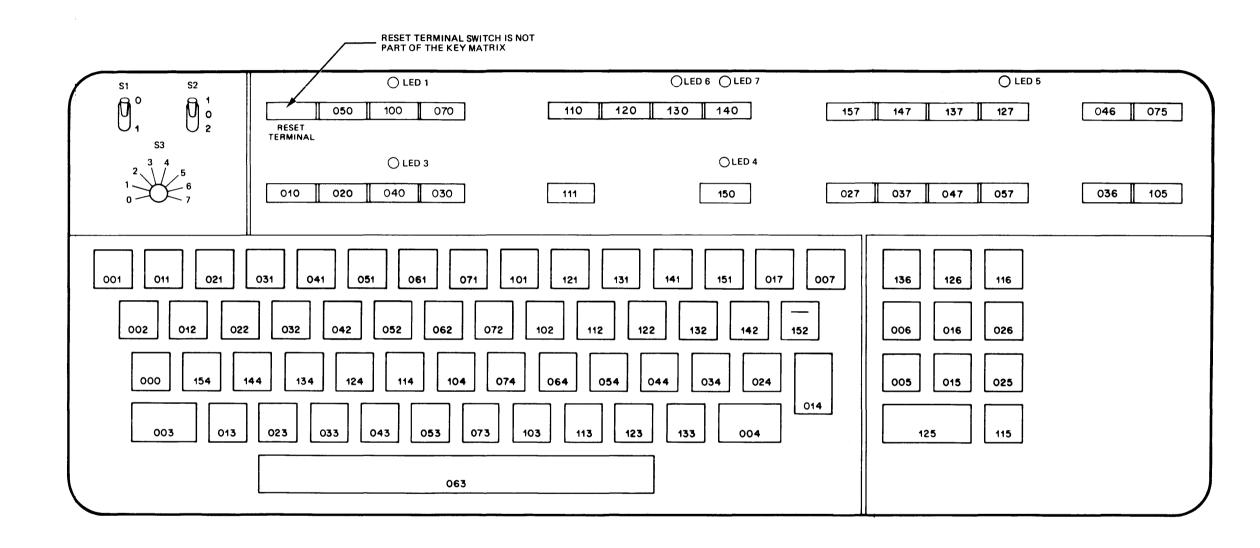
- 3.3 RAMP GENERATOR.
- 3.3.1 When a PD . COL15 signal is applied to the ramp generator, the current is enabled into the selected column. The current rises linearly from 0 to 80 milliamperes in 80 nanoseconds.
- 3.3.2 The ramp generator is a combination of a Miller integrator (transistor O1, resistor R35, and capacitor C5) and a current mirror (transistors

03 and 02). When the RD . COL15 signal is applied to the input, the collector of transistor Q1 falls to ground according to the transit time determined by R35 and C5. The collector current of Q1 rises linearly and is determined by resistors R39 and R36. Since the same base emitter voltage is applied to Q2 and Q3, the emitter current of Q2 "reflects" the current of Q3. This current flows through the selected column of the matrix. Nominally, the current rises from 0 to 80 milliamperes in 80 nanoseconds. The function of R37 (10 kilohms) is to bias the drive lines to ground when Q2 is off and no column is selected.

- 3.4 DIFFERENTIAL COMPARATORS.
- 3.4.1 The eight sense lines out of the key matrix are fed into the eight differential comparators. Each depressed switch generates a pulse on the corresponding row when its column is scanned. Differential comparators translate this pulse into the required TTL level.
- pifferential comparators are MC1414 or equivalent. Sense lines are fed into the minus input and are terminated by a 200-ohm resistor. Threshold is determined by resistor network (47K, 47K, 1.2K) and the previous state held in the input register. When the previous state is "0", the threshold is set to 200 millivolts and when "1", it is 100 millivolts. The differential comparator timing is shown in figure 4. When the processor wants to read one column, it puts the binary address of the column on the terminal data bus and the column decoder selects the corresponding drive line for the column. Approximately 200 nanoseconds

after the column has been selected, the RD . COL15 signal comes true and turns on the ramp generator. This current is transformed into sense lines only on those switches that are depressed. The sense pulse is approximately 400 millivolts in amplitude and 80 nanoseconds wide at the base. Differential comparators set the corresponding bits in the output register.

- 3.5 OUTPUT REGISTER. Outputs of the differential comparators set the corresponding bits in an 8-bit output register. Signals RD . COL15 and DATA OUT EN (on the Keyboard Interface PCA) enable the result of the selected column on the terminal data bus.
- INPUT REGISTER. Refore a column is read, the previous state of that column is sent from the processor to the input register. Outputs of this register determine the threshold of the differential comparators. If the previous state of a switch was "0", the threshold is 200 millivolts; if it was "1", then the threshold is 100 millivolts. This causes hysteresis in the key travel since the sense pulse amplitude is proportional to the key depression.
- pata comm Logic. The data communications logic contains a baud rate encoder and the keyboard data communications switches. Eight positions of the rotary BAUD RATE switch (Switch 3) are encoded into three binary bits. The 3-position PARITY switch (Switch 2), is encoded into two bits. The 2-position puplex switch (Switch 1) uses one bit for detection. When column 15 is addressed, the RD. COLIS (U8, Pin 10) signal is decoded and the data comm byte is released to the terminal data bus. (Refer to table 5.2 for more details.)
- 3.8 LFD REGISTER. The LED register is loaded with six data bits when a LED EN signal is decoded. Outputs of this latch drive six LED indicators.



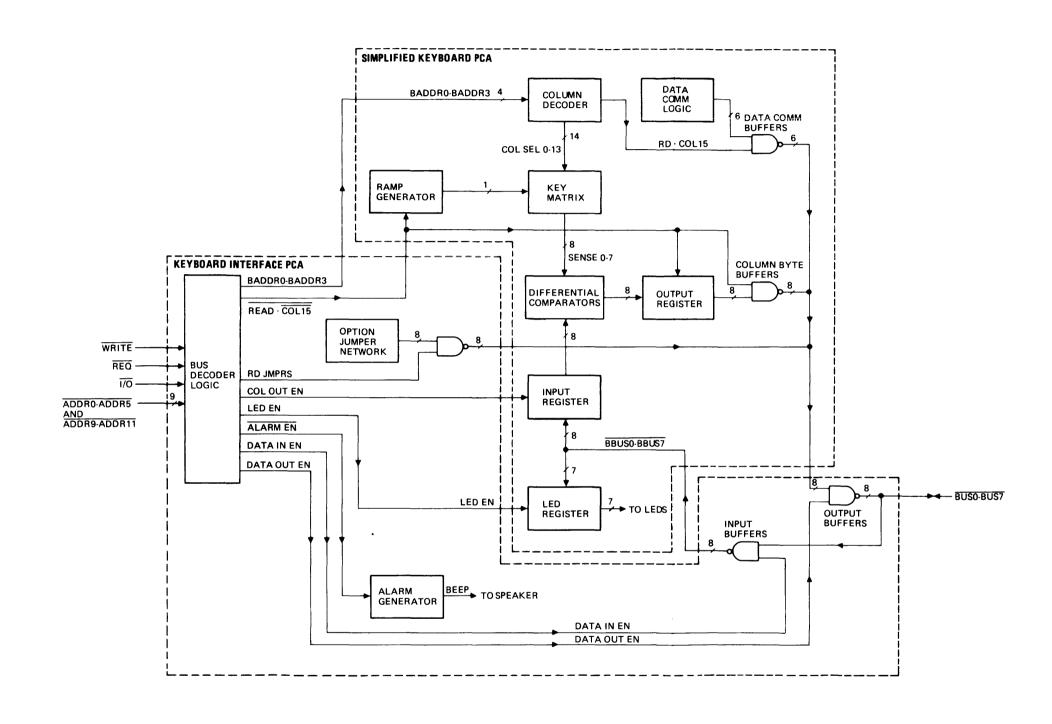


Figure ?
Simplified Keyboard Block Diagram
AUG-01-76 13255-91069

SIGHAL NAME	CONNECTOR # PIN NO.	
COLUMN DECODER OUTPUT	07,05	0.8V
RD.COL15	U8, PIN 6	0.7
CURRENT IN		80 NSec BOMA Ø MA
DIFFERENTIAL COMPARATOR INPUT	U12, U13, U15, U16	400 MY Ø MY
DIFFERENTIAL COMPARATOR OUTPUT	U12, U13, U15, U16	+5v ov
OUTPUT REGISTER	U14, U17	+5V

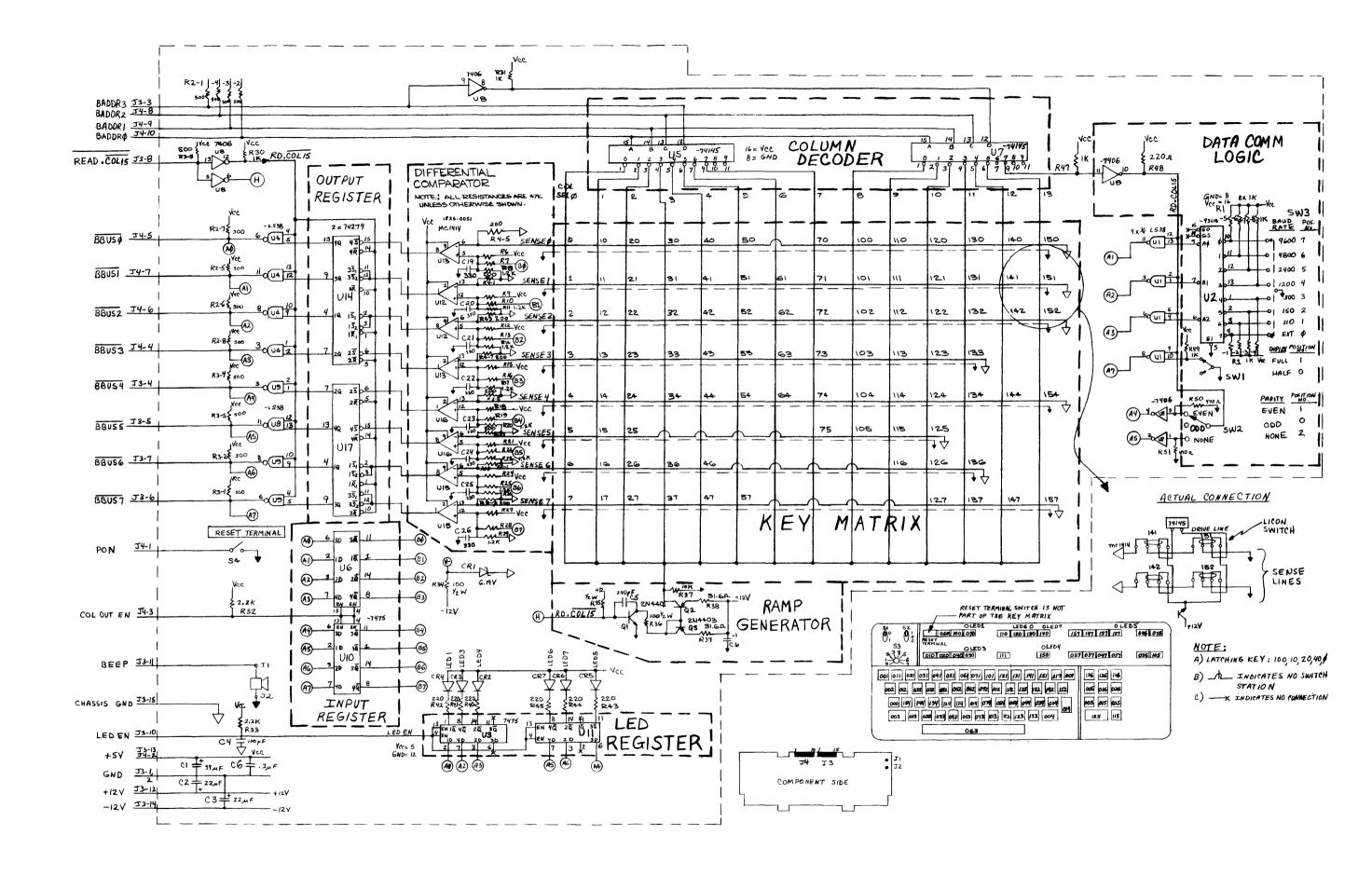
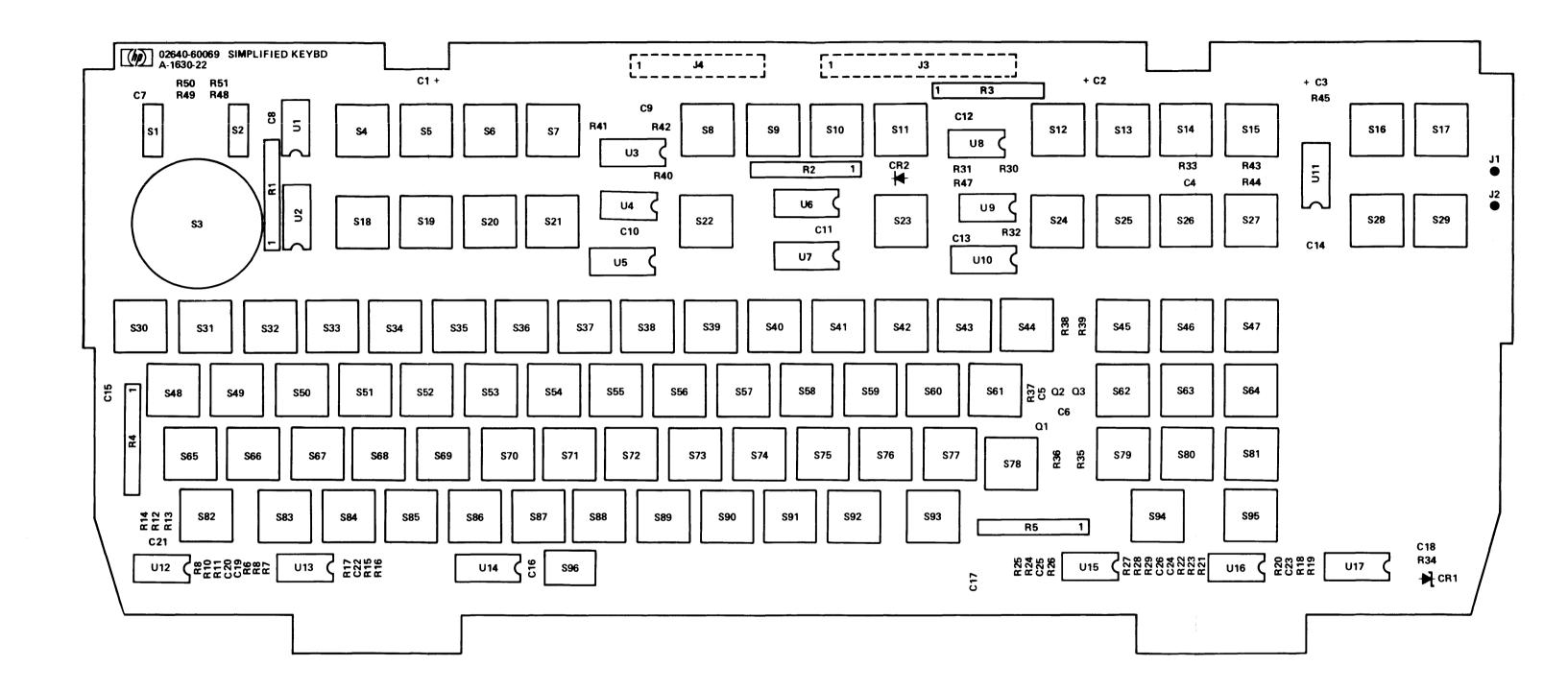


Figure 4
Differential Comparator Timing Diagram
AUG-01-76
13255-91069



Reference	HP Part	Qty	Description	Mfr	Mfr Part Number
Designation	Number			Code	······································
Al	02 640-60063	1	SIMPLIFIED KEYBOARD ASSEMBLY REVISION DATE: 03-26-76	28480	02640-60068
	04C3-0243 0403-0324 14C0-0440 2190-0918 2360-0193	2 4 1 8 2	BUMPER, FOOT, WHITE RUBBER .38" M BUMPER FOOT, GRAY PLASTIC 0-560" M CABLE TIE .062-1.75-DIA .184-MD NYL MASHER-LA HLCL NO6 .141-IN-IC SCREW-MACH 6-32 .25-IN-LG PAN-HD-POZI	00000 76381 59730 28480 28480	OBD SJ-5025-GRAY TY 35-0918 2360-0193
	2360-0201 2360-0203 3050-0066 7120-1927 7120-4403	1 5 6 1 1	SCREM-MACH 6+32 .5-IN-LG PAN-HC-POZI SCREW-MACH 6-32 .625-IN-LG PAN-HD-POZI WASHER-FI MILC ND-6 .147-IN-lD BLANK LABEL .625-IN-WD 1.5-IN-LG AL INFORMATION LABEL 1.98-IN-WD 2.187-IN-LG	28480 28480 28480 28480 28480	2360-0201 2360-0203 3050-0066 7120-1927 7120-4403
	9160-0233 02640-00022 02640-40007 02640-40021 02640-60041	1 1 1 1	LOUDSPEAKER OVERLAY, FULL KEYBOARD BASE, KEYBOARD KEYBOARD, TOP ASSY, SPEAKER CABLE	28480 28480 28480 28480 28480	9160-0233 02640-00022 02640-40007 02640-40021 02640-60041
	02640-60069 02640-60081	1	ASSY, SIMPLIFIED KEYBOARD ASSY, CABLE KEYBOARD	28480 28480	02640-68069 02640-68081
		:			
		İ			

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
	02640-60069	1	SIMPLIFIED KEYBOARD ASSEMBLY DATE CODE: A-1630-22	28480	02640-6006 9
(1 (2 (3 (4	01 E0-0393 01 E0-0228 01 E0-0228 01 E0-2204 01 40-0199	1 2 1 1	REVISION DATE: 08-25-76 CAPACITOR-FXD 39UF+-10% 10VDC TA CAPACITOR-FXD 22UF+-10% 15VDC TA CAPACITOR-FXD 22UF+-10% 15VDC TA CAPACITOR-FXD 100PF -5% 300HVDC MICA CAPACITOR-FXD 240PF +-5% 300HVDC MICA	56289 56289 56289 28480 72136	150D396X9010B2 150D226X9015B2 150D226X9015B2 0160-2204 DM15F241J0300WV1CR
66 67 68 69 610	0150-0121 0160-2055 0160-2055 0160-2055 0160-2055	1 12	CAPACITOR-FXD .1UF +80-20% 50NVDC CER CAPACITOR-FXD .01UF +80-20% 10CNVDC CER	28480 28480 28480 28480 28480	0150-0121 0160-2055 0160-2055 0160-2055 0160-2055
C11 C12 C13 C14 C15	0160-2055 0160-2055 0160-2055 0160-2055 0160-2055		CAPACITOR-FXD .01UF +80-20% 100WVDC CER CAPACITOR-FXD .01UF +80-20% 100WVDC CER CAPACITOR-FXD .01UF +80-20% 100WVDC CER CAPACITOR-FXD .01UF +80-20% 100WVDC CER CAPACITOR-FXD .01UF +80-20% 100WVDC CER	28480 28480 28480 28480 28480	0160-2055 0160-2055 0160-2055 0160-2055 0160-2055
C16 C17 C18 C19 C20	0160-2055 0160-2055 0160-2055 0160-3572 0160-3572	8	CAPACITOR-FXC .01UF +80-20% 100WVDC CER CAPACITOR-FXD .01UF +80-20% 100WVDC CER CAPACITOR-FXD .01UF +80-20% 100WVDC CER CAPACITOR-FXD 330PF +-10% 500WVDC CER CAPACITOR-FXD 330PF +-10% 500WVDC CER	28480 28480 28480 28480 28480	0160-2055 0160-2055 0160-2055 0160-3572 0160-3572
C21 C22 C23 C24 C25	0160-3572 0160-3572 0160-3572 0160-3572 0160-3572		CAPACITOR-FXD 330PF +-10% 500HVDC CER CAPACITOR-FXD 330PF +-10% 500HVDC CER CAPACITOR-FXD 330PF +-10% 500HVDC CER CAPACITOR-FXD 330PF +-10% 500HVDC CER CAPACITOR-FXD 330PF +-10% 500HVDC CER	28480 28480 28480 28480 28480	0160-3572 0160-3572 0160-3572 0160-3572 0160-3572
£26	0160-3572		CAPACITOR-FXD 330PF +-10% 500WVDC CER	28480	0160-3572
CR1 CR2	1962-0049 1990-0486	1	DIODE-ZNR 6-19V 5% DO-7 PD=.4W TC=+-022% LED-VISIBLE LUM-INT=1MCD IF=20MA-MAX	28480 28480	1902-0049 1990-0486
E1 E2 E3 E4 E5	0360-0124 0360-0124 0360-0124 0360-0124 0360-0124	5	TERMINAL-STUD SGL-PIN PRESS-MTG TERMINAL-STUD SGL-PIN PRESS-MTG TERMINAL-STUD SGL-PIN PRESS-MTG TERMINAL-STUD SGL-PIN PRESS-MTG TERMINAL-STUD SGL-PIN PRESS-MTG	28480 28480 28480 28480 28480	0360-0124 0360-0124 0360-0124 0360-0124 0360-0124
J3 J4	1251-3198 1251-3475	1	CUNNECTOR 15-PIN M POST TYPE CONNECTOR 10-PIN M POST TYPE	27264 27264	09-60-1151(2403-15A) 09-60-1101
01 02 03	1854-0467 1853-0271 1853-0271	1 2	TKANSISTOR NPN 2N4401 SI TO-92 PD=310MW TKANSISTOR PNP 2N4403 SI TO-92 PD=310MW TRANSISTOR PNP 2N4403 SI TO-92 PD=310MW	04713 04713 04713	2N4401 2N4403 2N4403
#1 #2 #3 #4 #5	1810-0121 1810-0132 1810-0132 1810-0163 1810-0163	1 2 2	NETWORK-RES 9-PIN-SIP .15-PIN-SPCG NETWORK-RES 9-PIN-SIP .15-PIN-SPCG NETWORK-RES 9-PIN-SIP .15-PIN-SPCG NETWORK-RES 9-PIN-SIP .15-PIN-SPCG NETWORK-RES 9-PIN-SIP .15-PIN-SPCG	28480 28480 28480 28480 28480	1810-0121 1810-0132 1810-0132 1810-0163 1810-0163
RD K7 K0 K9 K10	0683-4735 0683-4735 0683-1225 0683-4735 0683-4735	16 8	RESISTOR 47K 5% .25W FC TC=-400/+800 RESISTOR 47K 5% .25W FC TC=-400/+800 RESISTOR 12K 5% .25W FC TC=-400/+700 RESISTOR 47K 5% .25W FC TC=-400/+800 RESISTOR 47K 5% .25W FC TC=-400/+800	01121 01121 01121 01121 01121	CB4735 CB4735 CB1225 CB4735 CB4735
K11 K12 K15 K14 K15	06 83-1225 06 83-4735 06 83-4735 06 83-1225 06 83-4735		RESISTOR 1.2K 5% .25W FC TC=-400/+700 RESISTOR 47K 5% .25W FC TC=-400/+800 RESISTOR 47K 5% .25W FC TC=-400/+800 RESISTOR 1.2K 5% .25W FC TC=-400/+700 RESISTOR 47K 5% .25W FC TC=-400/+800	01121 01121 01121 01121 01121	CB1225 CB4735 CB4735 CB1225 CB4735
K10 R17 K18 R19 K20	0663-4735 0663-1225 0683-4735 0663-4735 0663-1225		RESISTOR 47K 5% .25M FC TC=-40C/+800 RESISTOR 1.2K 5% .25M FC TC=-40C/+700 RESISTOR 47K 5% .25M FC TC=-40C/+800 RESISTOR 47K 5% .25M FC TC=-40C/+800 RESISTOR 1.2K 5% .25M FC TC=-40C/+700	01121 01121 01121 01121 01121	CB4735 CB1225 CB4735 CB4735 CB1225
KZ1 K22 K23 K24 K25	0683-4735 0683-4735 0683-1225 0683-4735 0683-4735		RESISTOR 47K 5% -25W FC TC=-400/+800 RESISTOR 47K 5% -25W FC TC=-400/+800 RESISTOR 1-2K 5% -25W FC TC=-400/+700 RESISTOR 47K 5% -25W FC TC=-400/+800 RESISTOR 47K 5% -25W FC TC=-400/+800	01121 01121 01121 01121 01121	C84735 C84735 C81225 C81225 C84735 C84735
K20 K27 R28 K29 K30	06 63-1225 06 63-4735 06 63-4735 06 63-1225 06 63-1025	4	RESISTOR 1.2K 5% .25W FC TC=-400/+700 RESISTOR 47K 5% .25W FC TC=-400/+800 RESISTOR 47K 5% .25W FC TC=-400/+800 RESISTOR 1.2K 5% .25W FC TC=-400/+700 RESISTOR 1K 5% .25W FC TC=-400/+600	01121 01121 01121 01121 01121	CB1225 CB4735 CB4735 CB1225 CB1025

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
R31 R32 R33 R34 R35	06 63-1025 06 63-2225 06 63-2225 06 66-1015 06 58-3402	2 1 1	SIMPLIFIED KEYBOARD ASSEMBLY, CONT'D. RESISTOR 1K 5% -25m FC TC=-400/+600 RESISTOR 2-2K 5% -25m FC TC=-400/+700 RESISTOR 2-2K 5% -25m FC TC=-400/+700 RESISTOR 100 5% -5m CC TC=0+529 RESISTOR 316 1% -5m F TC=0+-100	01121 01121 01121 01121 01121 91637	C81Q25 C82225 C82225 E81Q15 MFF-1/2-1Q
K30 K37 K38 K39 K40	0757-0198 0683-1035 0757-0180 0757-0180 0683-2215	1 1 2 7	RESISTOR 100 1% .5M F TC=0+-100 RESISTOR 10K 5% .25M FC TC=-400/+700 RESISTOR 31.6 1% .125M F TC=0+-100 RESISTOR 31.6 1% .125M F TC=0+-100 RESISTOR 220 5% .25M FC TC=-400/+600	19701 01121 24546 24546 01121	MF7C1/2-T0-101-F C81035 C4, T-0 C4, T-0 C82215
K41 K42 K43 K44 K45	06 & 3-2215 06 & 3-2215 06 & 3-2215 06 & 3-2215 06 & 3-2215		RESISTOR 220 5% .25M FC TC=-400/+600 RESISTOR 220 5% .25M FC TC=-400/+600	01121 01121 01121 01121 01121	CB2215 CB2215 CB2215 CB2215 CB2215 CB2215
K47 K48 R49 R50 R51	06 E3-1025 06 E3-2215 06 E3-1025 06 E3-4715 06 E3-4715	2	RESISTOR 1K 52 .25W FC TC=-400/+600 RESISTOR 220 5% .25W FC TC=-400/+600 RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 470 5% .25W FC TC=-400/+600 RESISTOR 470 5% .25W FC TC=-400/+600	01121 01121 01121 01121 01121	CB1025 CB2215 CB1025 CB4715 CB4715
SN1 SM2 SM3 SM4 SM5	31 C1-1858 31 C1-1859 31 C0-3313 31 C1-1745 31 O1-1899	1 1 1 1 87	SWITCH-TGL SUBMIN SPDT NS .02A 20VAC/DC SWITCH-TGL SUBMIN SPDT NS .02A 20VAC/DC SWITCH-RTRY .812 IN CTR SPCG IDX-ANG=36 SWITCH-PB SPST-NO MOM .12A 28VAC SWITCH-PB SPST-NO MOM .08A	09353 09353 28480 28480 28480	7101-L2YCBE 7103-L2YCBE 3100-3313 3101-1745 3101-1899
SM6 SM7 SM8 SM9 SM10	31 C1-1900 31 C1-1899 31 C1-1899 31 C1-1899 31 C1-1899	5	SWITCH-PB SPST-NO ALTNG .OBA SWITCH-PB SPST-NO MOM .OBA SWITCH-PB SPST-NO MOM .OBA SWITCH-PB SPST-NO MOM .OBA SWITCH-PB SPST-NO MOM .OBA	28480 28480 28480 28480 28480	3101-1900 3101-1899 3101-1899 3101-1899 3101-1899
SW11 SW12 SW13 SW14 SW15	31 C1-1899 31 C1-1899 31 C1-1899 31 C1-1899 31 C1-1899		SWITCH-PB SPST-NO MOM .O8A SWITCH-PB SPST-NO MOM .O8A SWITCH-PB SPST-NC MOM .O8A SWITCH-PB SPST-NC MOM .O8A SWITCH-PB SPST-NC MOM .O8A	28480 28480 28480 28480 28480	3101-1899 3101-1899 3101-1899 3101-1899 3101-1899
SW16 SW17 SW18 SW19 SW20	31 C1-1899 31 C1-1899 31 C1-1900 31 C1-1900 31 C1-1900		SWITCH-PB SPST-NO MOM .08A SWITCH-PB SPST-NO MOM .08A SWITCH-PB SPST-NO ALTNG .08A SWITCH-PB SPST-NO ALTNG .08A SWITCH-PB SPST-NO ALTNG .08A	28480 28480 28480 28480 28480	3101-1899 3101-1899 3101-1900 3101-1900 3101-1900
S N 2 1— S N 6 4 S N 6 6— S N 9 5	31G1-1899 31G1-1900 31G1-1899		SMITCH-PB SPST-NO MOM .08A SMITCH-PB SPST-NO ALTNG .08A SWITCH-PB SPST-NC MOM .08A	28480 28480 28480	3101-1899 3101-19 9 0 3101-1899
U1 U2 U3 U4	1820-1209 1820-0987 1820-0301 1820-1209 1820-0491	3 1 4	IC-DIGITAL SN74LS38N TTL LS QUAD 2 NAND IC-DIGITAL 93L18PC TTL L 8 IC-DIGITAL SN7475N TTL D-TYPE IC-DIGITAL SN74LS38N TTL LS QUAD 2 NAND IC-DIGITAL SN74LS38N TTL LS QUAD 2 NAND IC-DIGITAL SN74145N TTL 4 BCD-TO-DEC	01295 07263 01295 01295 01295	SN74L S38N 93L 18PC SN7475N SN74L S36N SN74L S36N
U6 U7 U8 U9 U1U	1820-0301 1820-0491 1820-0471 1820-1209 1820-0301	1	IC-DIGITAL SN7475N TTL D-TYPE IC-DIGITAL SN74145N TTL 4 BCD-TO-DEC IC-DIGITAL SN7406N TTL HEX I IC-DIGITAL SN74058N TTL LS QUAD 2 NAND IC-DIGITAL SN7475N TTL LD-TYPE	01295 01295 01295 01295 01295	SN7475N SN74145N SN7406N SN74L538N SN7475N
U11 U12 U13 U14 U15	1820-0301 1826-0051 1826-0051 1820-1089 1826-0051	4 2	IC-DIGITAL SN7475N TTL D-TYPE IC MC 1414 COMPARATOR IC MC 1414 COMPARATOR IC-DIGITAL SN74279N TTL QUAD IC MC 1414 COMPARATOR	01295 04713 04713 01295 04713	SN7475N MC1414P MC1414P SN74279N MC1414P
U16 U17	1826-0051 1820-1089		IC MC 1414 COMPARATOR IC-DIGITAL SN74279N ITL QUAD MISCELLANEOUS	04713 01295	MC1414P SN74279N
	03 60-0040 03 60-0268 03 70-0620 03 70-1129 03 70-2260	3 1 1 1	TERMINAL-SLDR LUG LK-MTG FÜR-#1/4-SCR TERMINAL-SLDR LUG LK-MTG FÖR-#6-SCR KEYCAP, DBLO KNOB KEYCAP, 1	73734 78189 28480 28480 28480	1958 2103-06-00 0370-0620 0370-1129 0370-2260
	0370-2261 0370-2262 0370-2263 0370-2264 0370-2265	1 1 1 1	KEYCAP, 2" KEYCAP, 3# KEYCAP, 4\$ KEYCAP, 52 KEYCAP, 6C	28480 28480 28480 28480 28480	0370-2261 0370-2262 0370-2263 0370-2264 0370-2265

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
	0370-2266 0370-2267 0370-2268 0370-2270 0370-2271	1 1 1 1	SIMPLIFIED KEYBOARD ASSEMBLY, CONT'D. KEYCAP, 8(KEYCAP, 9) KEYCAP, A KEYCAP, B	28480 28480 28480 28480 28480	0370-2266 0370-2267 0370-2268 0370-2270 0370-2271
	0370-2272 0370-2273 0370-2274 0370-2275 0370-2276	1 1 1 1	KEYCAP, C KEYCAP, D KEYCAP, E KEYCAP, F KEYCAP, G	28480 28480 28480 28480 28480	0370-2272 0370-2273 0370-2274 0370-2275 0370-2276
	0370-2277 0370-2278 0370-2279 0370-2280 0370-2281	1 1 1 1	KEYCAP, H KEYCAP, I KEYCAP, J KEYCAP, K KEYCAP, L	28480 28480 28480 28480 28480	0370-2277 0370-2278 0370-2279 0370-2280 0370-2281
	0370-2282 0370-2283 0370-2284 0370-2285 0370-2286	1 1 1 1	KEYCAP, M KEYCAP, N KEYCAP, O KEYCAP, R KEYCAP, Q	28480 28480 28480 28480 28480	0370-2282 0370-2283 0370-2284 0370-2285 0370-2286
	0370-2287 0370-2288 0370-2289 0370-2290 0370-2291	1 1 1 1	KEYCAP, R KEYCAP, S KEYCAP, T KEYCAP, U KEYCAP, V	28480 28480 28480 28480 28480	0370-2287 0370-2288 0370-2289 0370-2290 0370-2291
	0370-2292 0370-2293 0370-2294 0370-2295 0370-2296	1 1 1 1 1	KEYCAP, W KEYCAP, X KEYCAP, Y KEYCAP, Z KEYCAP, Z	28480 28480 28480 28480 28480	0370-2292 0370-2293 0370-2294 0370-2295 0370-2296
	0370-2297 0370-2298 0370-2312 0370-2313 0370-2314	1 1 1 1	KEYCAP, > KEYCAP, ?/ KEYCAP, 1 KEYCAP, 2 KEYCAP, 3	28480 28480 28480 28480 28480	0370-2297 0370-2298 0370-2312 0370-2313 0370-2314
	0370-2315 0370-2316 0370-2317 0370-2318 0370-2319	1 1 1 1	KEYCAP, 4 KEYCAP, 5 KEYCAP, 6 KEYCAP, 7 KEYCAP, 8	28480 28480 28480 28480 28480	0370-2315 0370-2316 0370-2317 0370-2318 0370-2319
	0370-2322 0370-2322 0370-2324 0370-2325 0370-2635	1 1 1 1 1	KEYCAP, 9 KEYCAP, +; KEYCAP, +; KEYCAP, COLIGN KEYCAP, RETURN	28480 28480 28480 28480 28480	0370-2320 0370-2322 0370-2324 0370-2325 0370-2635
	03 70-2636 03 70-2637 03 70-2641 03 70-2644 03 70-2646	2 1 1 9 1	KEYCAP, SHIFT KEYCAP, COTL KEYCAP, TONCTION KEYCAP, ESC	28480 28480 28480 28480 28480	0370-2636 0370-2637 0370-2641 0370-2646 0370-2646
	0370-2647 0370-2648 0370-2649 0370-2650 0370-2651	1 1 1 1 1	KEYCAP, LOCK KEYCAP, =- KEYCAP, LINE FEED KEYCAP, DEL- KEYCAP, SLASH	28480 28480 28480 28480 28480	0370-2647 0370-2648 0370-2649 0370-2650 0370-2651
	03 70-2652 03 70-2653 03 70-2654 03 70-2655 03 60-0371	1 1 1 0	KEYCAP, BKT LEFT KEYCAP, BKT TAT KEYCAP, BKT TAT KEYCAP, APPROX KEYCAP, AT SPACER-RND 375LG -171ID -250D FBR	28480 28480 28480 28480 83339	0370-2652 0370-2653 0370-2654 0370-2655 2131
	03 60-0585 0470-0231 0850-1134 1450-0528 1460-0691	4 1 6 1	STANDOFF-RVT-ON .531LG 6-32THD .250D BRS CUMPOUNDB NUT LOCK TUBING, UBBER 3/16" LAMP SOCKET BIPIN-SKT BIPIN-TERM PC SPRING-TRSN .531-IN-N 4-IN-LG MUN PLUNGER FOR MTG SPACE BAR MECH:STL NP	28480 28480 28480 28480 28480	0470-0231 0470-0231 0890-1134 1450-0528 1460-0691
	1530-1737 1530-1738 2190-0047 2360-0192 2360-0197	2 2 1 4 1	HOUSING, SPACE BAR, END HOUSING FOR MTG WASHER-LK 82 CTSK EXT T NO6 .142-IN-ID SCREN-MACH 6-32 .25-IN-LG 100 DEG SCREN-MACH 6-32 .375-IN-LG PAN-HD-POZI	04426 78189 28480 28480	80-550774 1506-00 2360-0192 2350-0197
	29 50-0052 30 50-0099 81 51-0013 02 640-00003 02 640-00019 02640-00041	2 2 1 1	NUT-HEX-DBL-CHAM 1/4-40-THD .062-THK MASHER-FL HILC NO12 .25-IN-ID .5-IN-OD WIRE 22AWG 1X22 PLATE, SUITCH MOUNTING KEY CAP- SPACE SPRING, CONTACT	28480 28480 28480 28480 28480	290-0094 3050-0099 8151-0014 02640-00003 02640-00019
		İ			